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			F	ebruary 16,	2007		(Signature) (Date)
APPLICATION NO.	FILING DATE		FIRST NAMED INVENTOR	А	TTORNEY	DOCKET NO.	CONFIRMATION NO.
10/675,965 09/30/2003 Sion C. Quinlan 30022/US/3 6302 ITLE OF INVENTION: SEMICONDUCTOR PACKAGE ASSEMBLY AND METHOD FOR ELECTRICALLY ISOLATING MODULES							
APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE F	EE TOT	AL FEE(S) DUE	DATE DUE
nonprovisional	МО	\$1400	\$300	\$0		\$1700	02/28/2007
EXAMI	NER	ART UNIT	CLASS-SUBCLASS				
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PLEASE NOTE: Unle	ess an assignee is identi in 37 CFR 3.11. Comp	fied below, no assignee	THE PATENT (print or typ data will appear on the pa T a substitute for filing an a (B) RESIDENCE: (CITY	itent. If an 12 12 12 12 12 12 12 12 12 12 12 12 12	ONTRY)	#EP=168863#52409	1400.00 OP
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ease check the appropria	ate assignee category or	categories (will not be pr	rinted on the patent):	Individual X Corp	oration or	other private group	p entity Government
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